INFORMATION DISCLOSURE STATEMENT BY APPLICANTS PTO-1449

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Applicant(s) Martin Vorbach et al.	
Filing Date	Group Art Unit
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U.S. PATENT DOCUMENTS

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	Culler, D.E; Singh, J.P., "Parallel Computer Architecture," Page 17, 1999, Morgan Kaufmann, San Francisco, CA USA, XP002477559.		
	Short, Kenneth L., Microprocessors and Programmed Logic, Prentice Hall, Inc., New Jersey 1981, p. 34.		
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EXAMINER	/Thuy Dao/	DATE CONSIDERED 12/07/2010	
EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not			

considered. Include copy of this form with next communication to applicant.